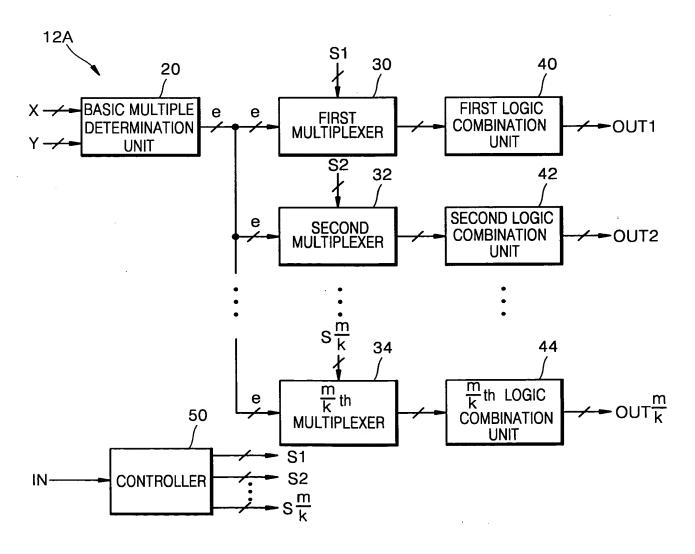
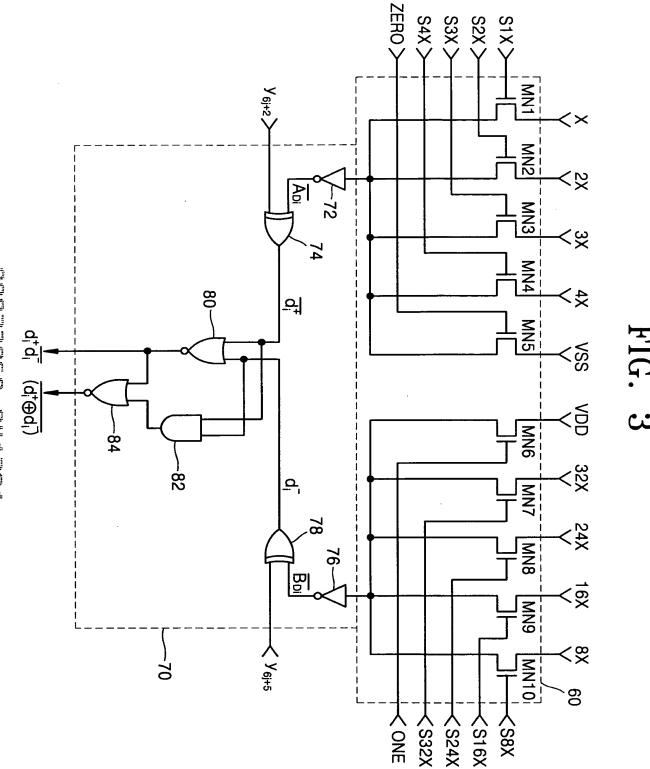


FIG. 2



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FIG. 4

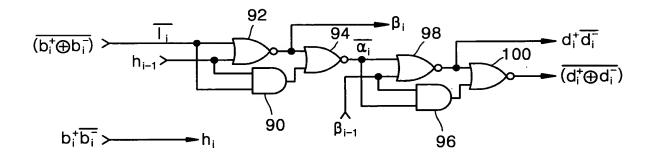


FIG. 5

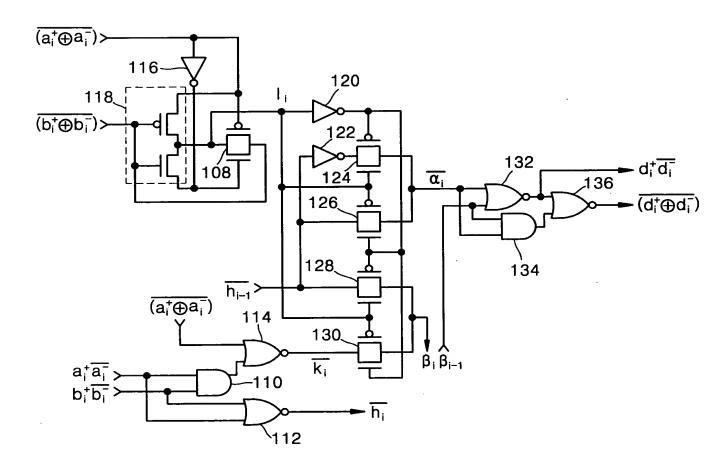






FIG. 6

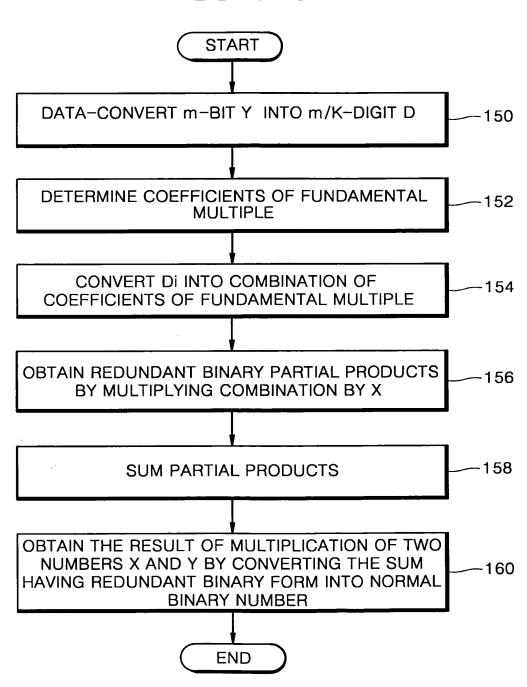
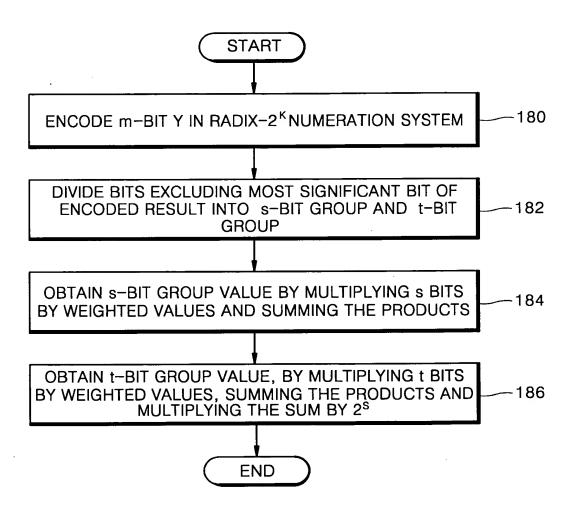




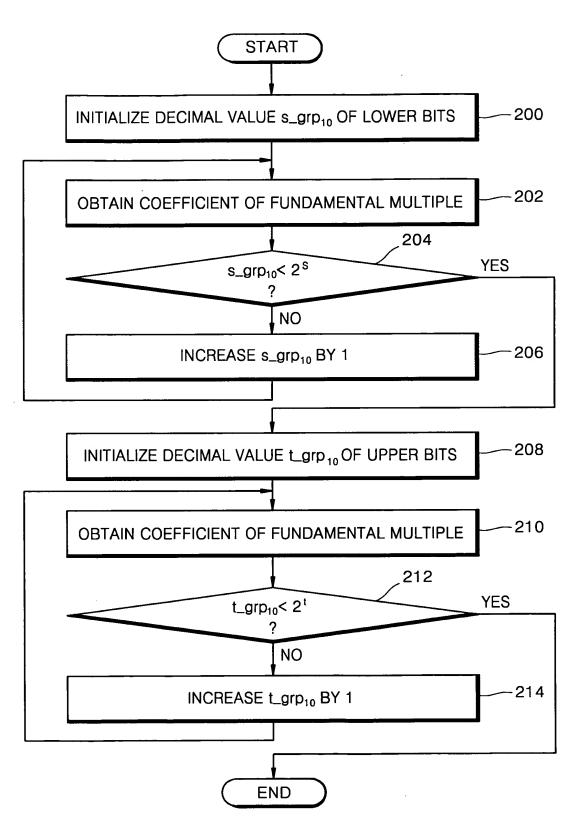
FIG. 7





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FIG. 8



Atty Docket No.: 401164/YPLEE Leydig, Voit & Mayer, Ltd. 202-737-6770 FIG. START 240 NO TS MSB OF Yi "1 YES INVERT EACH OF THE BITS OF Yi 242 244 IS THE NO FOURTH BIT IA FROM LSB OF Y; "1" YES INVERT EACH OF THE LOWER BITS OF YI - 246 OBTAIN t-BIT GROUP VALUE AND s-BIT GROUP VALUE USING LOWER BITS 250 AND UPPER BITS OF Yi, RESPECTIVELY 252 NO IS IA"1" 254 256 YES OBTAIN "A" BY MULTIPLYING s-BIT GROUP OBTAIN "A" BY MULTIPLYING s-BIT GROUP VALUE BY "X" AND INVERTING THE PRODUCT VALUE BY "X" OBTAIN "B" BY MULTIPLYING t-BIT GROUP VALUE 258 BY "X" AND INVERTING THE PRODUCT 260 NO IS IA"1" 264 262 YES SET CORRECTION BITS (EX $^{+}$, EX $^{-}$) TO BE (0,1) SET CORRECTION BITS (EXT, EXT) TO BE (0,0) 266 NO IS MSB OF Y; "1" YES TRANSPOSE THE POSITIONS OF "A" AND "B" AND 268 TRANSPOSE THE POSITIONS OF EXTAND EXT TO STEP 158

Title: Apparatus And Method For Digital Multiplication Using Redundant Binary Arithmetic Inventors: PARK et al.